CS 314 Operating Systems, Spring 2021

Study Guide for Week #6

Note: This is NOT the question list. Quiz question list will be e-mailed to each of you later.

#1: What are the two inefficiency problems in “processes”?

1) Slow context-switching (high context-switching overhead)

2) Slow sharing data (processes: shared memory – messages queues, threads: global variables) = high IPC overhead

#2: What are “threads”?

#3: How do threads reduce the high context switching overhead in processes?

#4: How do threads reduce the high overhead of processes when processes try to share data in shared memory?

#5: Show the typical internal structure of a process that consists of multiple threads.

#6: Show the typical internal structure of a thread.

#7: What information do multiple threads in a process can share?

#8: Can threads in a process share global variables in other processes? If yes, briefly describe how. If not, briefly explain why not.

#9: What information must be managed in TCB (thread control block, also known as “private PCB”) and what information should be still in the PCB (also known as “global PCB”)?

#10: Which of the following items in the PCB for a process should belong to the global PCB or TCB (private PCB)?

(a) Processor registers
(b) Program Counter (PC) register
(c) User ID
(d) Process ID
(e) The list of opened files
(f) The list of the assigned I/O devices
(g) Stack Pointer (SP) register

#11: Which of PCB (global PCB) or TCB (private PCB for each thread) do processor registers belong to? Briefly (but with a proper emphasis on the essential reason) justify your opinion.
#12: Which of PCB (global PCB) or TCB (private PCB for each thread) does Program Counter (PC) register belong to? Briefly (but with a proper emphasis on the essential reason) justify your opinion.

#13: Which of PCB (global PCB) or TCB (private PCB for each thread) does User ID (UID) information belong to? Briefly (but with a proper emphasis on the essential reason) justify your opinion.

#14: Most of the operating systems today includes Stack Pointer (SP) register in their private PCB (i.e., “TCB”). What are the advantages in this design? Mention at least two different advantages.

#15: As we discussed in the classroom, “threads” are introduced after many system programmers were using “processes” for multi-tasking (we even discussed that “threads” were introduced to avoid two problems in “processes”). After all, while “processes” and “threads” have many things in common (and “threads” seem to be better than “processes”). Then why do we still use “processes” (mention at least two different reasons)?

#16: What are the two different implementations of “threads”?

#17: What are the advantages in the kernel-mode threads?

#18: What are the advantages in the user-mode threads?

#19: Complete the following table that compares the user-mode and kernel-mode threads.

<table>
<thead>
<tr>
<th>Factors</th>
<th>User-Mode</th>
<th>Kernel-Mode</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preemptive thread scheduling</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Robustness</td>
<td></td>
<td></td>
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<tr>
<td>Execution speed</td>
<td></td>
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<tr>
<td>Portability</td>
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