CS 314 Operating Systems, Fall 2018
Quiz #5 on September 26, 2017
List of the possible questions

PART I: “left-over topics”

#1: What is the primary difference between “multi-boot” and “micro-kernel architecture”?

#2: Describe how “micro-kernel architecture” and “non micro-kernel architecture” are different in how system calls issued by user applications will be executed.

#3: What are the advantages in using “micro-kernel architecture”? What is the primary disadvantage in “micro-kernel architecture”?

#4: What are the two processor modes operating systems set?

#5: What is “kernel (system) mode” of a processor?

#6: What are “system calls”?

#7: What is “user mode” of a processor?

#8: Why do operating systems have two processor modes (what problems will happen if there is no “user mode” – all programs run in the kernel (system) mode)?

#9: What is the primary difference between “multi-boot” and “virtual machine”?

PART II: Threads

#10: What are the two inefficiency problems in “processes”?

#11: What are “threads”?

#12: How do threads reduce the high context switching overhead in processes?

#13: How do threads reduce the high overhead of processes when processes try to share data in shared memory?

#14: Show the typical internal structure of a process that consists of multiple threads.

#15: Show the typical internal structure of a thread.

#16: What information do multiple threads in a process can share?

#17: Can threads in a process share global variables in other processes? If yes, briefly describe how. If not, briefly explain why not.
#18: What information must be managed in TCB (thread control block, also known as “private PCB”) and what information should be still in the PCB (also known as “global PCB”)?

#19: Which of the following items in the PCB for a process should belong to the global PCB or TCB (private PCB)?

(a) Processor registers  
(b) Program Counter (PC) register  
(c) User ID  
(d) Process ID  
(e) The list of opened files  
(f) The list of the assigned I/O devices  
(g) Stack Pointer (SP) register

#20: Which of PCB (global PCB) or TCB (private PCB for each thread) do processor registers belong to? Briefly (but with a proper emphasis on the essential reason) justify your opinion.

#21: Which of PCB (global PCB) or TCB (private PCB for each thread) does Program Counter (PC) register belong to? Briefly (but with a proper emphasis on the essential reason) justify your opinion.

#22: Which of PCB (global PCB) or TCB (private PCB for each thread) does User ID (UID) information belong to? Briefly (but with a proper emphasis on the essential reason) justify your opinion.

#23: Most of the operating systems today includes Stack Pointer (SP) register in their private PCB (i.e., “TCB”). What are the advantages in this design? Mention at least two different advantages.

#24: As we discussed in the classroom, “threads” are introduced after many system programmers were using “processes” for multi-tasking (we even discussed that “threads” were introduced to avoid two problems in “processes”). After all, while “processes” and “threads” have many things in common (and “threads” seem to be better than “processes”). Then why do we still use “processes” (mention at least two different reasons)?

#25: What are the two different implementations of “threads”? 

#26: What are the advantages in the kernel-mode threads?

#27: What are the advantages in the user-mode threads?

#28: Complete the following table that compares the user-mode and kernel-mode threads.

<table>
<thead>
<tr>
<th>Factors</th>
<th>User-Mode</th>
<th>Kernel-Mode</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preemptive thread scheduling</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Robustness</td>
<td></td>
<td></td>
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<tr>
<td>Execution speed</td>
<td></td>
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<tr>
<td>Portability</td>
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